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APPLICANT
Hideki TORIKOSHI et al.

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
<i>CU</i>	A1	5,642,285	6/97	Woo et al.	364	449.7	

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FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

CW	A5	PATENT ABSTRACT OF JAPAN, Vol. 1998, No. 06, Minami Kikuchi, "System and Method of Automatic
		Correction for Computer, Built-In Watch", April 30, 1998, Publication #10031083, Publication Date 2/98 ✓
CW	A6	PATENT ABSTRACT OF JAPAN, Vol. 2000, No. 10, Hideki Nakamura, "Satellite Communications Equipment,
		and Circuit For Maintaining Synchronization Thereof" January 3, 2001, Pub. #2000224091, ✓
		Pub. Date 8/00

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